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A6 chip The Apple A6
chip is the mobile
processor developed
by Apple for use in the
iPhone 5 and the

iPhone 5S. It was announced in October 2011 and was first released as a prototype, followed by the final production version in July 2012.

The A6 was not designed specifically

for the iPhone 5, but rather is an upgrade to the iPhone 4S's A5 processor. The A5 became the main CPU for the iPhone 4S while the A6 was a minor upgrade for iPhone 5. The A5 was succeeded

by the A5X. Design
The A6 is a
7-nanometer
processor,
manufactured using a
45 nm process, that
combines an array of
eight processing cores
with a dedicated

graphics core. The A6
is the first Apple
processor with a
significant GPU core,
being based on the
next-generation
graphics core, the
Imagination
Technologies PowerVR

SGX 543MP2 GPU,
which uses double the
number of shader units
(20) and introduce 32
texture units. The A6
has 80 movable macro-
ops, 64 kFloat multiply-
adds, 192 kFloat
vector multiply-adds

and 96 kByte floating-point image unit on the same die as the CPU and GPU. The logic used to run the machine code instructions in the design of the A6 includes a micro-

architecture called
High Performance
Scalar Unit (HPSU),
which utilizes a mix of
dual-issue and single-
issue concurrent
execution to improve
performance of some
use cases of vector

arithmetic. The design of the A6 incorporates a new instruction set architecture called VFP, which is a superset of ARM's NEON. VFP uses a large variety of multimedia

instructions such as
integer and floating
point comparisons,
floating point
multiplies,
transcendental
function
implementations and
floating point vector

add operations which
are designed to be
more efficient when
performed on floating
point operations. The
new HPSU architecture
may reduce the
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